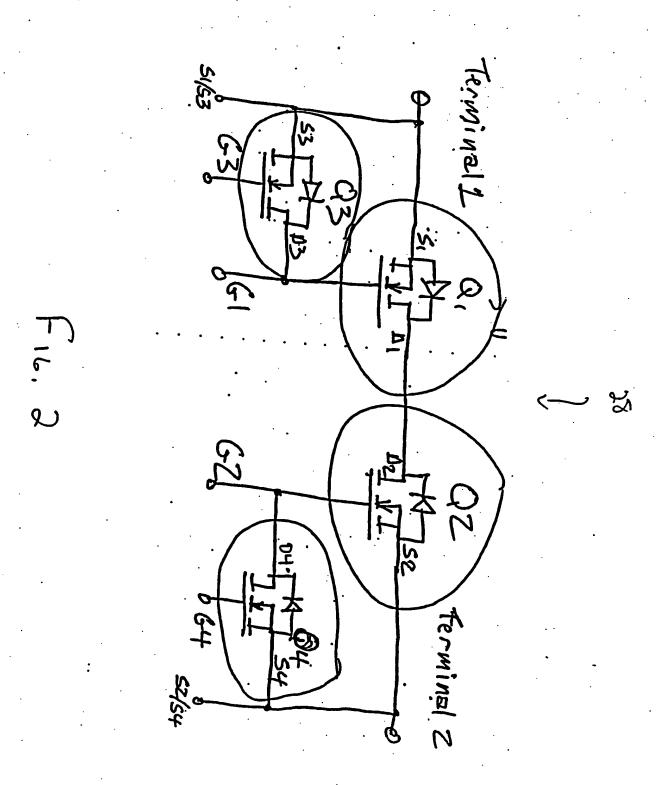
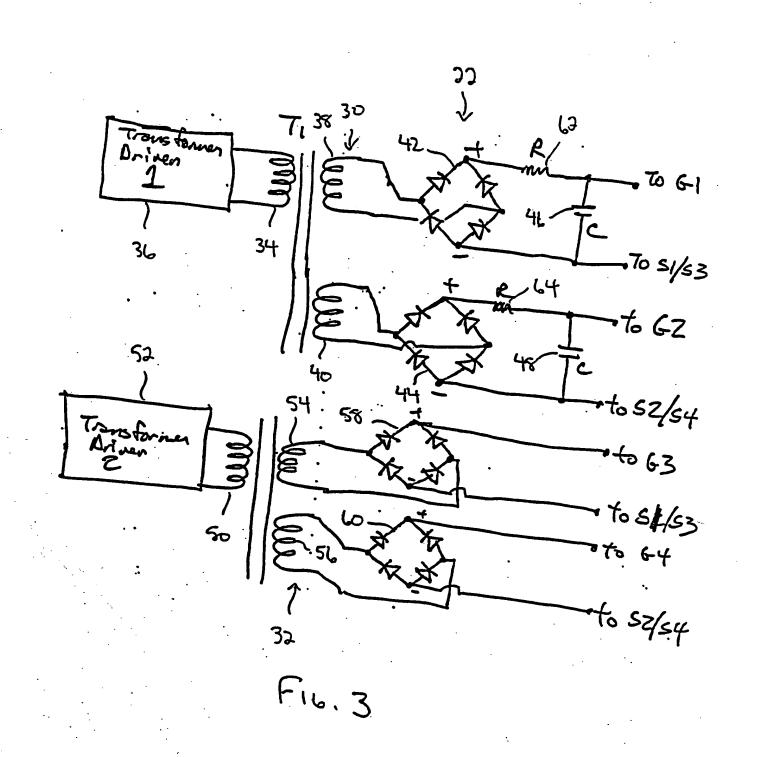
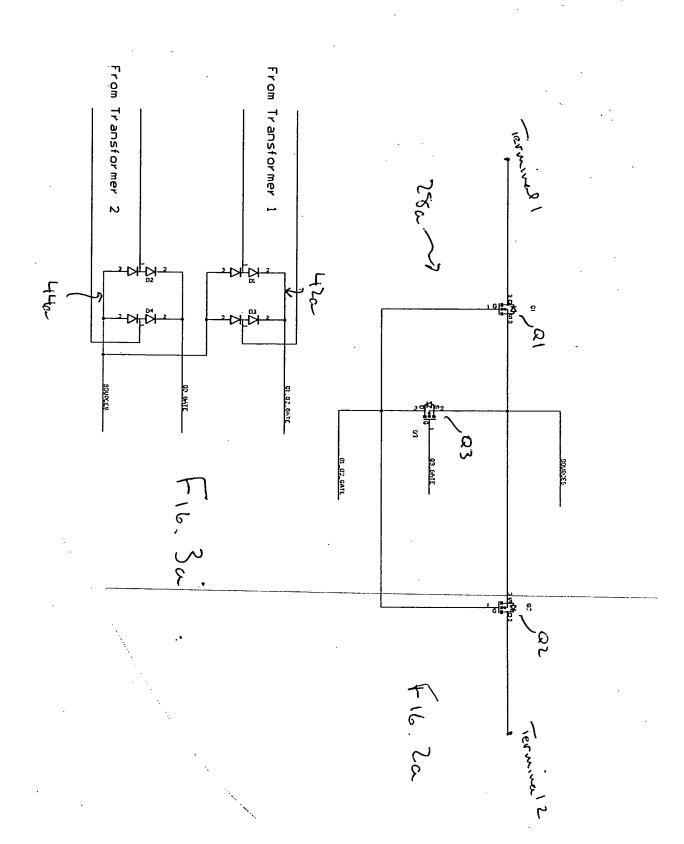
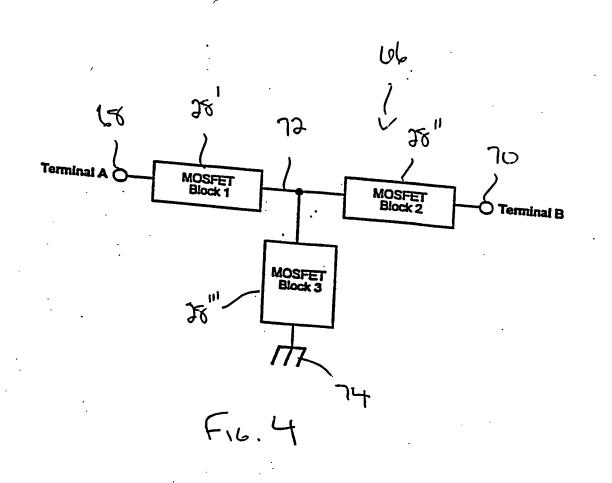


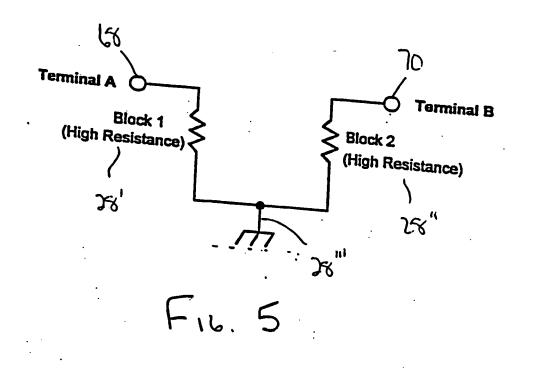
F16.1.

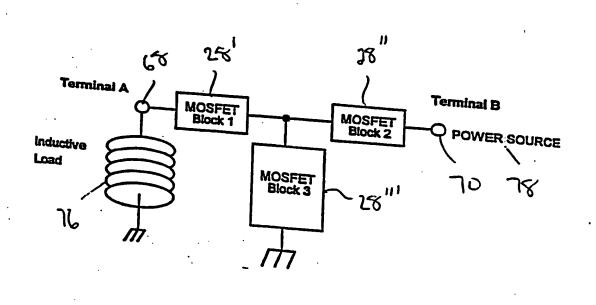






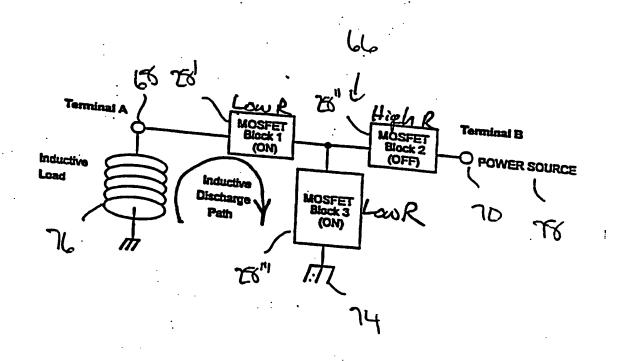




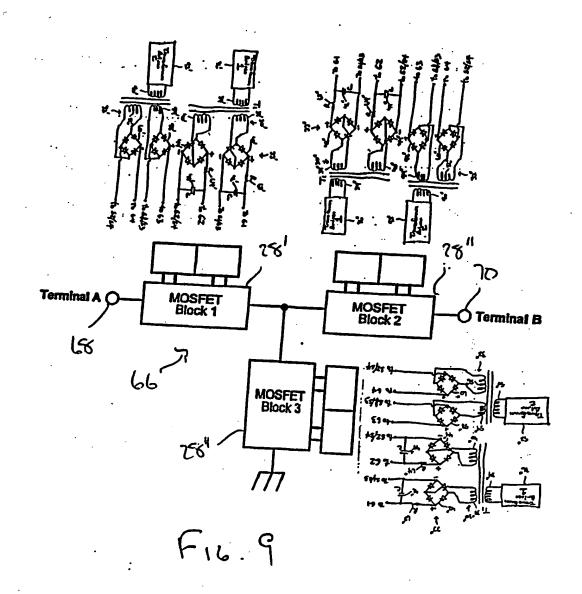


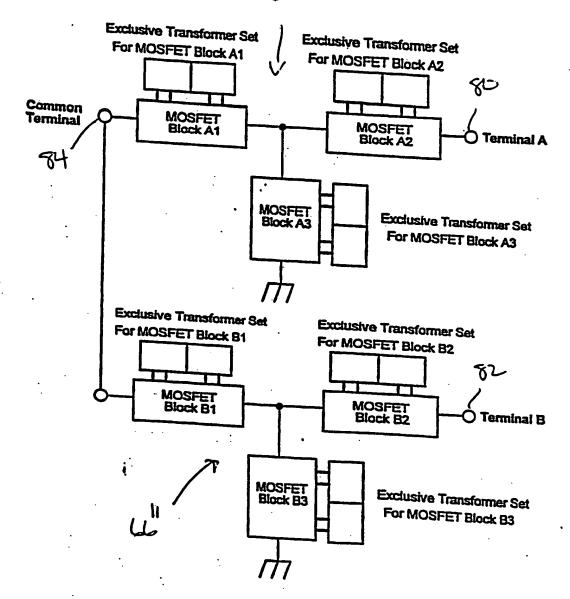
FIL. 6

Figure DC Power Source PRIOR ART. Inductive load with fositive down Soura and discharge (or flyback) diode igure : 7a Power Source PRIOR ART I uductive load with negative & flower Soura



F16.8





F112 10

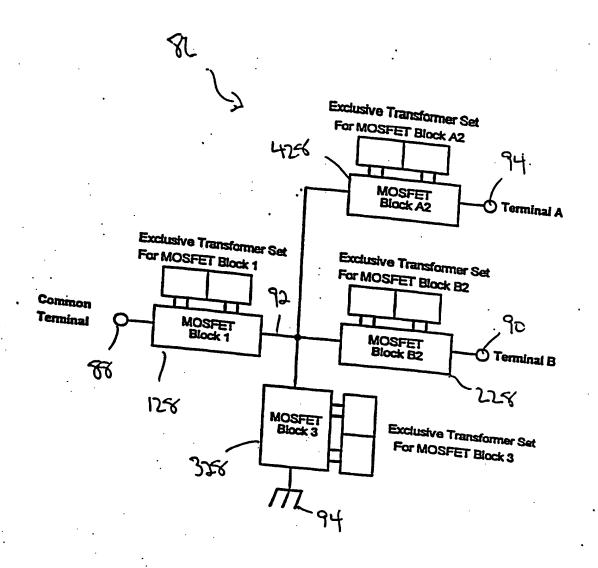
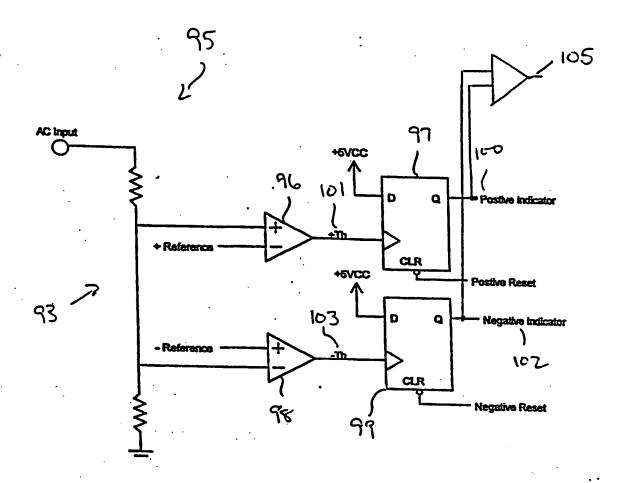
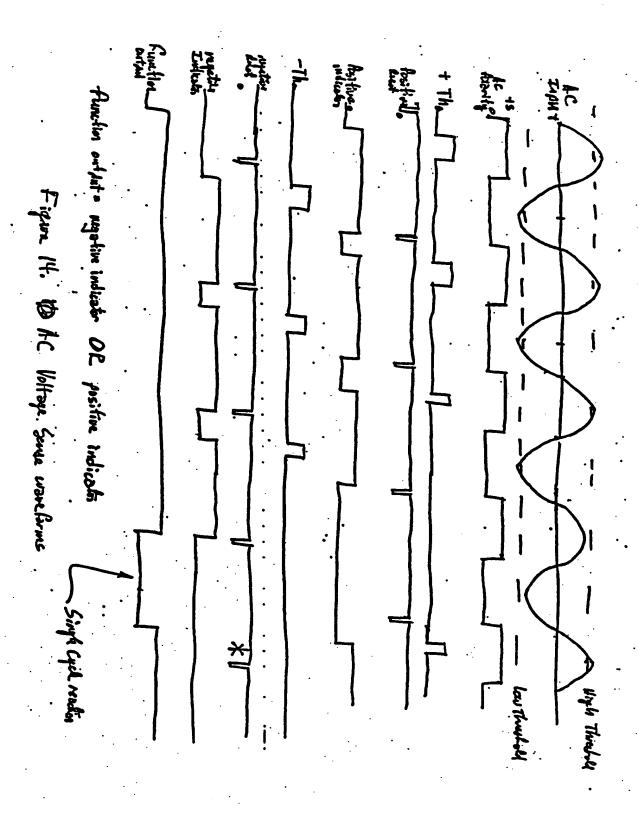


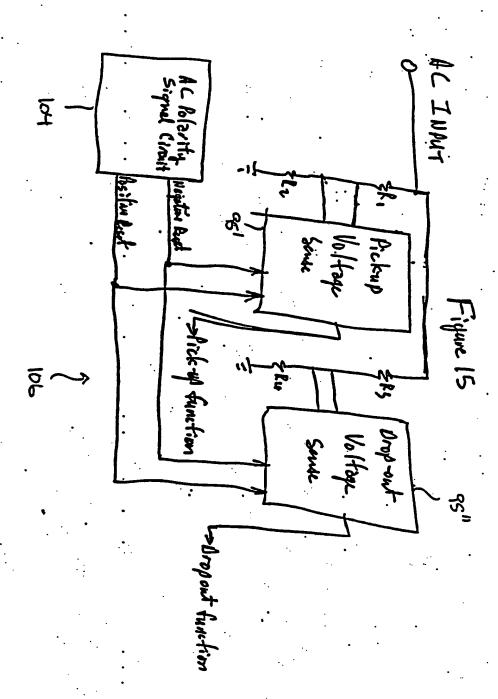
FIG II

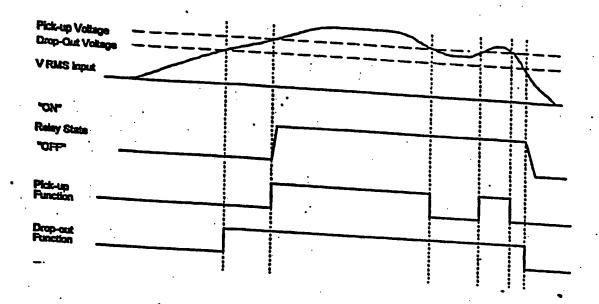


F16. 12

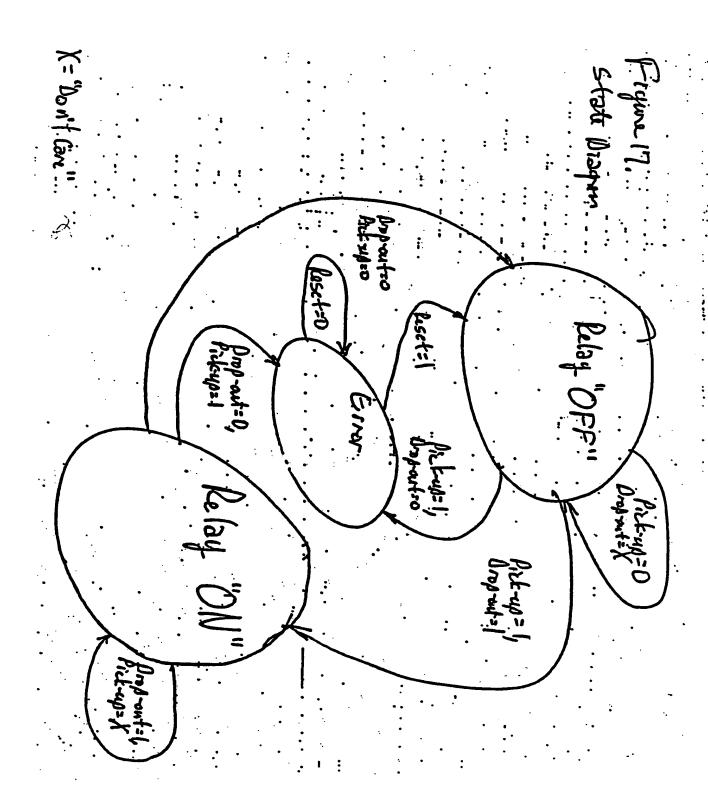
Figure 13 & C Polerity Signel Circuit
ACI HICH SPEED CLOCK







F16.16



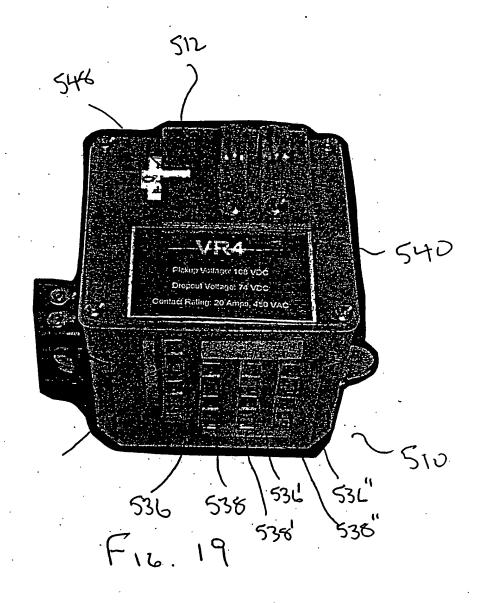
Ъ 108 حا10 101 106

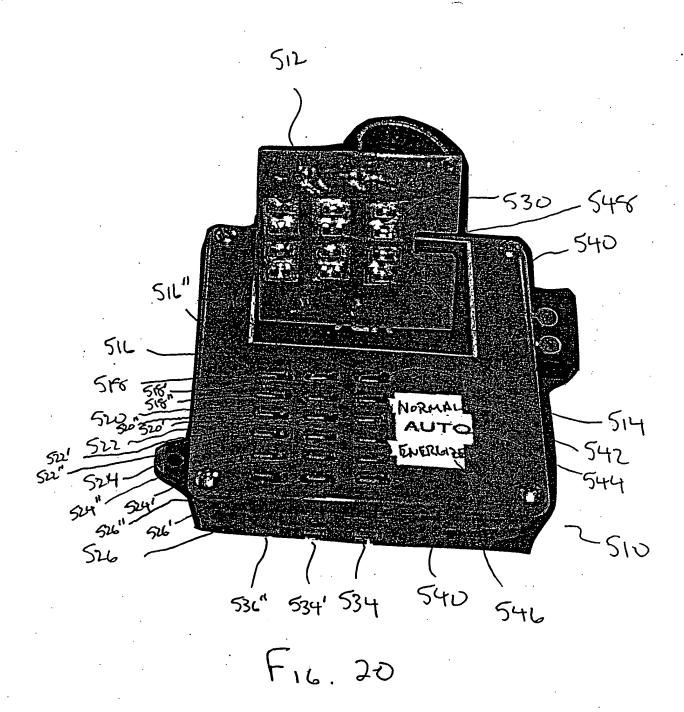
AC SENSE INPUT

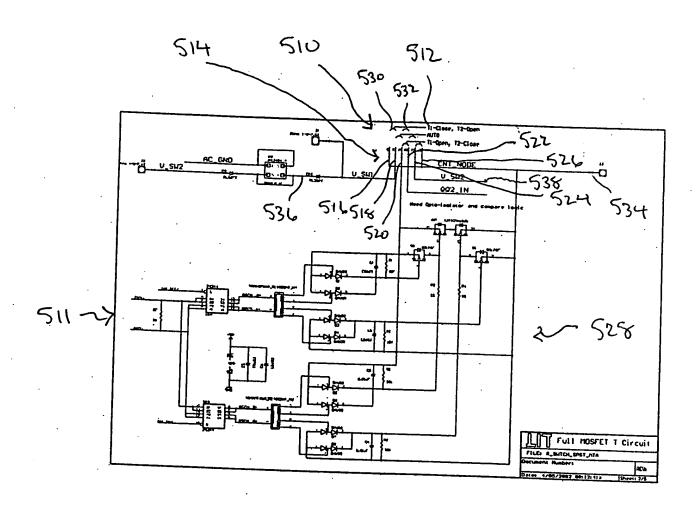
Threshold

Circuits

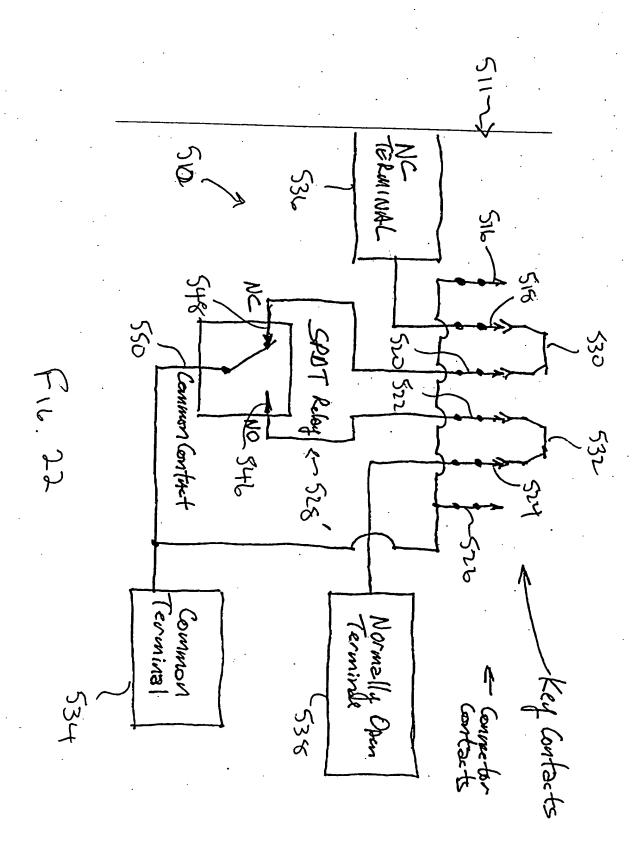
File 156







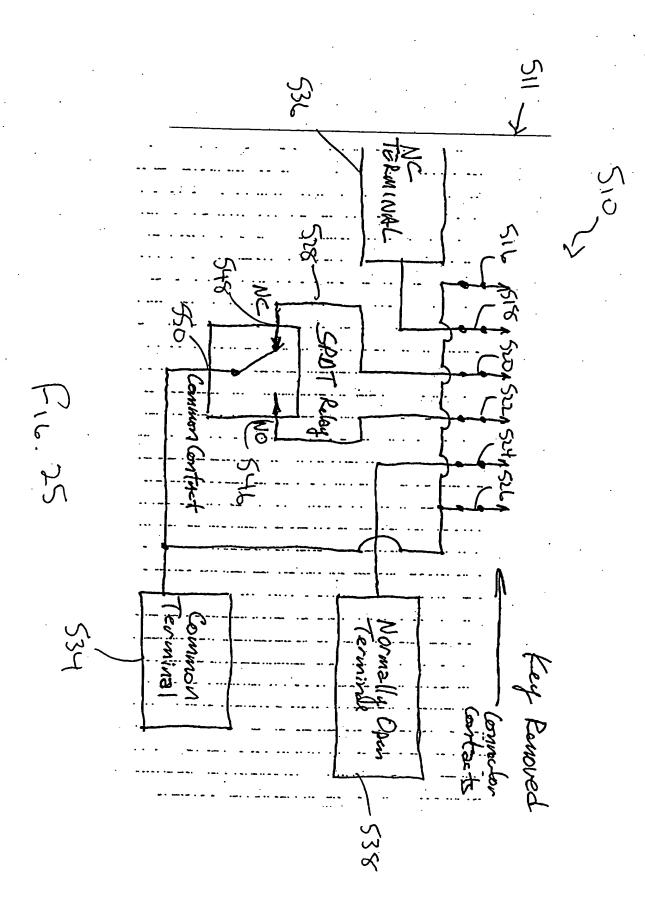
F16.21

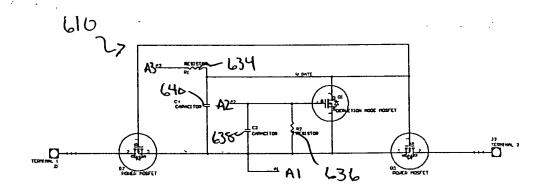


Mormal F16.23 Ker Contrac-

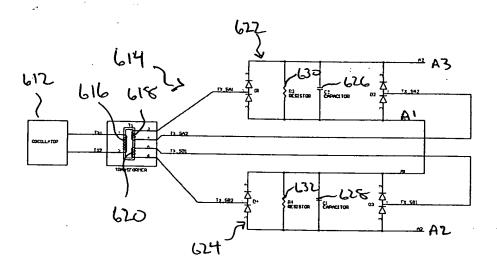
Energized Mode ley Confacts

he and





F16.26



F16.27

